

FIG 1

fCK MHz	Factor	MRS	tCK	tRAS (34)	Actual	Error	F _{exampl} MHz	Description	T _{exampl}	TRAS nom
118.00	1.00	1	8.47	5	40.4	42.4	8.4			
123.90	1.05	2	8.07	5	38.4	40.4	6.4	125.00	MIN	8.00
130.10	1.10	3	7.69	5	36.6	38.4	4.4			40.00
136.60	1.16	4	7.32	5	34.9	36.6	2.6			
143.43	1.22	5	6.97	6	39.8	41.8	7.8			
150.60	1.28	6	6.64	6	37.9	39.8	5.8			
158.13	1.34	7	6.32	6	36.1	37.9	3.9			
166.04	1.41	8	6.02	6	34.4	36.1	2.1	166.67	DDR2-333	6.00
174.34	1.48	9	5.74	7	38.2	40.2	6.2			36.00
183.06	1.55	10	5.46	7	36.4	38.2	4.2			
192.21	1.63	11	5.20	7	34.7	36.4	2.4	200.00	DDR2-400	5.00
201.82	1.71	12	4.95	8	37.8	39.6	5.6			35.00
211.91	1.80	13	4.72	8	36.0	37.8	3.8			
222.51	1.89	14	4.49	8	34.2	36.0	2.0			
233.63	1.98	15	4.28	9	36.7	38.5	4.5			
245.31	2.08	16	4.08	9	34.9	36.7	2.7			
257.58	2.18	17	3.88	10	37.0	38.8	4.8	266.67	DDR2-533	3.75
270.46	2.29	18	3.70	10	35.2	37.0	3.0			37.50
283.98	2.41	19	3.52	11	36.9	38.7	4.7			
298.18	2.53	20	3.35	11	35.1	36.9	2.9			
313.09	2.65	21	3.19	12	36.5	38.3	4.3			
328.74	2.79	22	3.04	12	34.8	36.5	2.5	333.33	DDR2-667	3.00
345.18	2.93	23	2.90	13	35.9	37.7	3.7			36.00
362.44	3.07	24	2.76	13	34.2	35.9	1.9			
380.56	3.23	25	2.63	14	35.0	36.8	2.8			
399.59	3.39	26	2.50	15	35.8	37.5	3.5	400.00	DDR2-800	2.50
419.57	3.56	27	2.38	15	34.0	35.8	1.8			37.50
440.55	3.73	28	2.27	16	34.6	36.3	2.3			
462.58	3.92	29	2.16	17	35.0	36.8	2.8			
485.70	4.12	30	2.06	18	35.3	37.1	3.1			
509.99	4.32	31	1.96	19	35.5	37.3	3.3			

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FIG 3

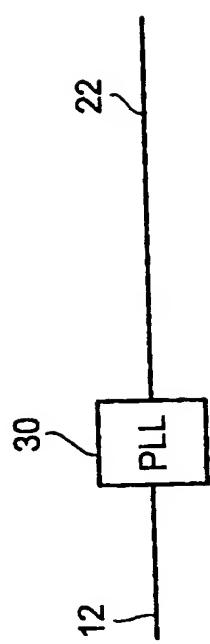
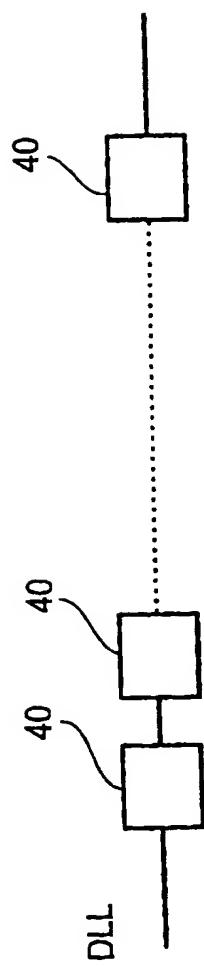


FIG 4



List of reference numerals

10 Memory element
12 Clock signal
14 Clock generator
16 Memory control
18 Signal bus
20 Timing control
22 Clock period duration register
24 Nominal delay time memory
26 Timing element
30 PLL
32 Faster clock signal
40 Reference delay elements